

402

406 (Frequency Domain) Digital Output To Second Subractor Stage (Optional) Spectral Signal-Processor 2 Digital (Spectral) Combining Unit Transform to time domain (Time Domain) Digital Output Amplitude Scaling ADC2 Digital Spectral Output Error Signal [Spectrally Shaped] ] Subtractor Spectral Signal-Processor 1 Delay Digital Combining Unit(optional) (Optional Time Domain) Digital Output Splitter ADC1 314 90 Input Analog Signal

Input Analog Signal

200

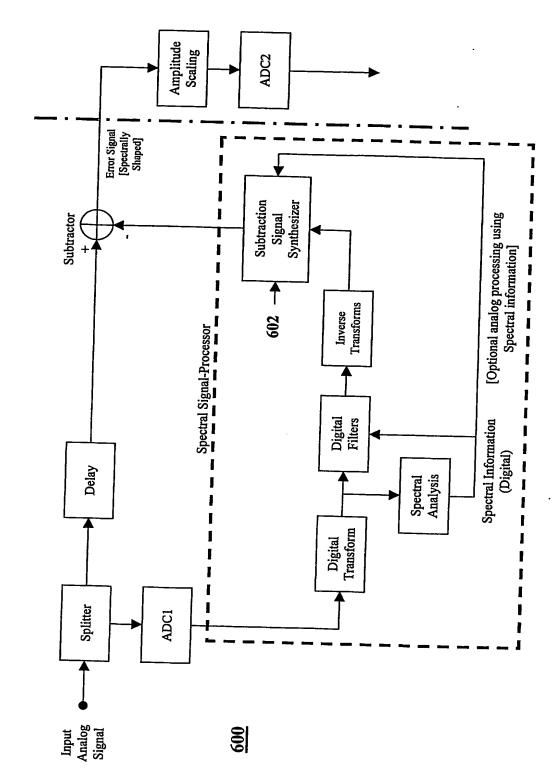
340

ADC2

Amplitude Scaling

Error Signal [Spectrally Shaped] Post-DAC Analog Filtering Subtractor Analog filtering using Spectral information [To reduce DAC1 Quantization noise & errors] DAC1 I i 509 1 Inverse Transform 512 508 Spectral-Signal Processor Digital Filtering 206 Spectral Information Delay Spectral Analysis Digital Transform 502 Splitter ADC1 > 908

FIG. 6





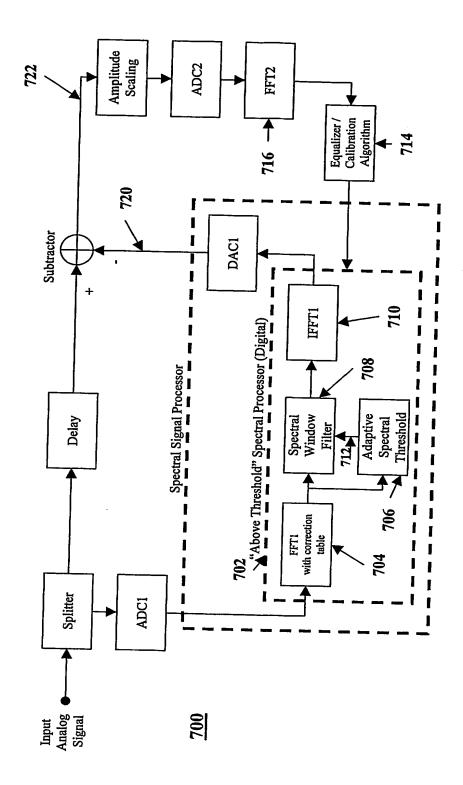
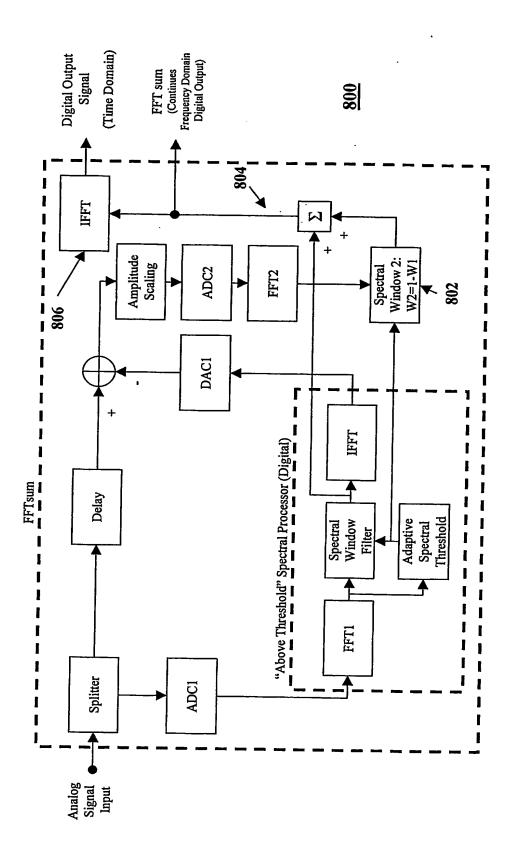
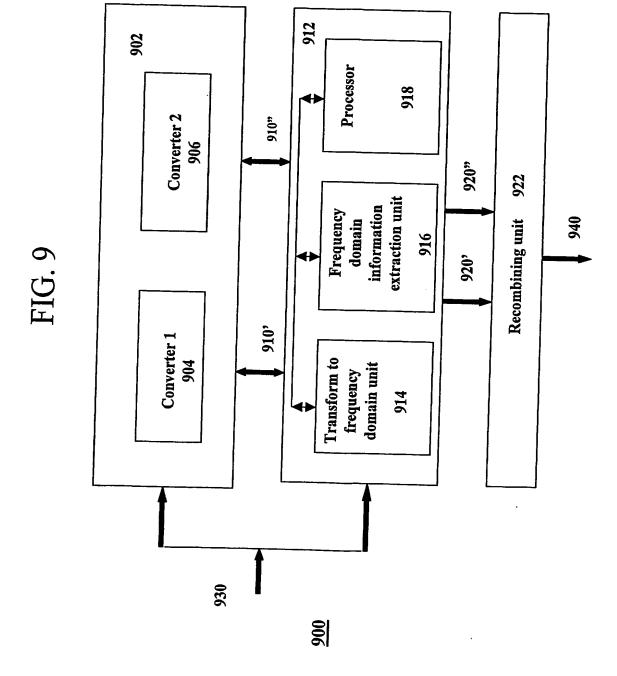
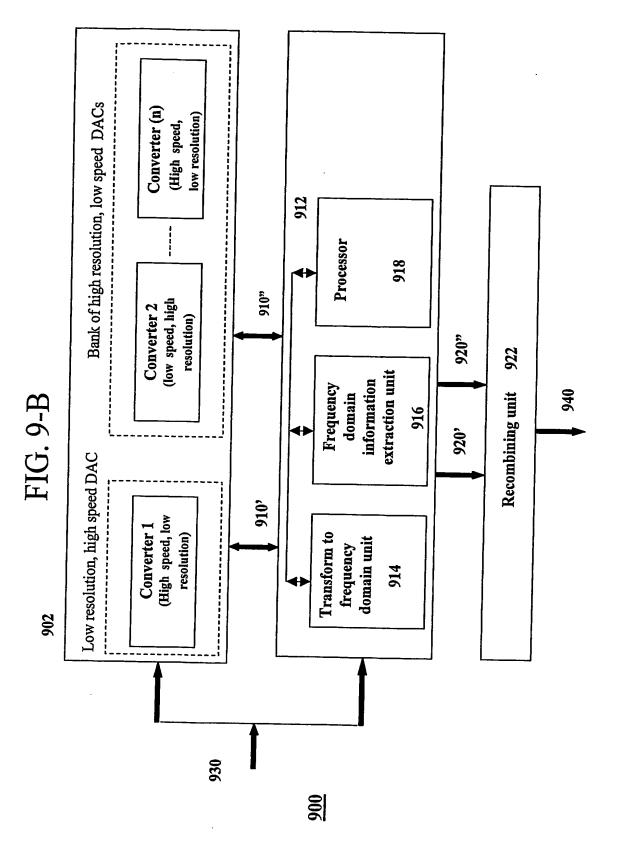
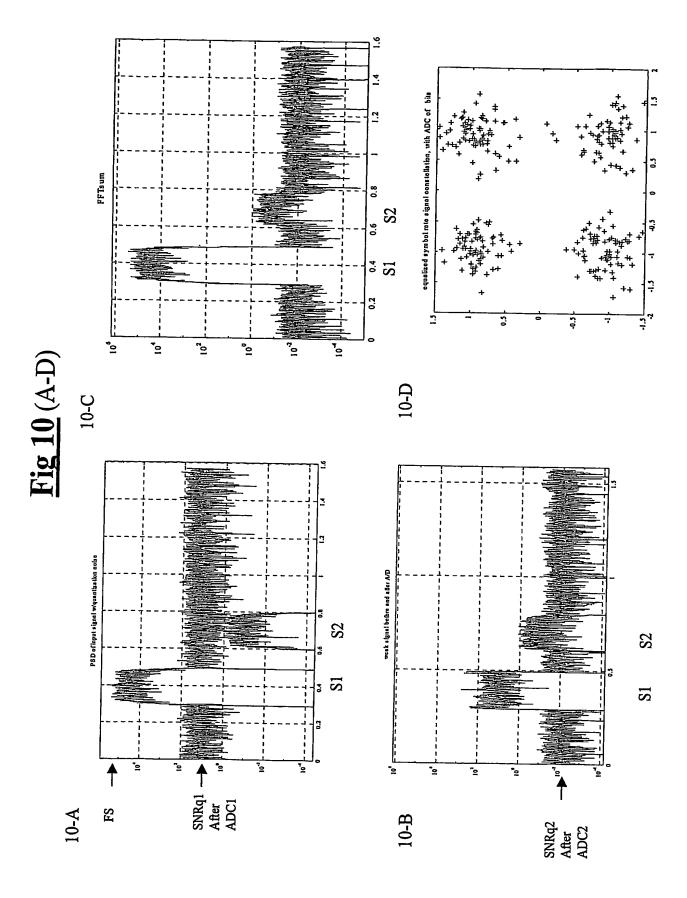


FIG. 8









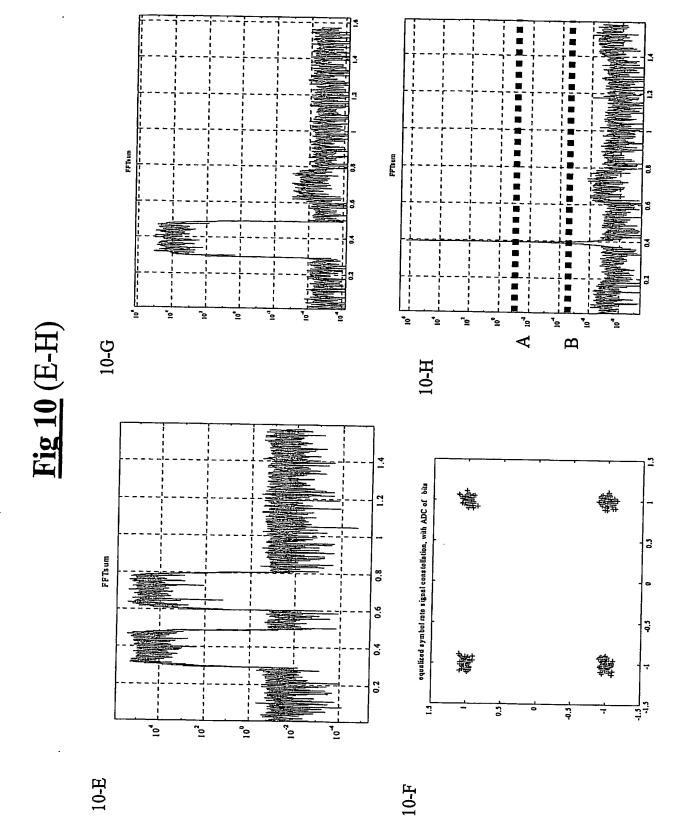


Fig 11 (A-C)

